

Presenters and their biographies

(morning sessions)



Dr. Hanspeter SCHMID was an analog-IC designer with Bernafon AG, Switzerland, until 2005, where he mainly worked on audio low-noise amplifiers, voltage regulators, and a wireless transceiver, and was also responsible for full-system signal integrity. Now he is a Research Fellow at IME/FHNW and a senior lecturer at ETHZ. His main research interests are fast low-power circuits (mainly for sensor electronics), signal integrity in analog signal processing, sigma-delta conversion and mixed-analog-digital signal processing. He also does consulting in industry projects. In addition to his technical work, he occasionally works as a conflict moderator or facilitator, and he gives communication courses and conflict prevention courses for engineers and for laymen. Hanspeter Schmid was IEEE CAS Analog Signal Processing Technical Committee Co-Chair from 2008-2010 and still is a committee member; he is an Associate Editor of TCAS-I, a member of the ESSCIRC technical committee, and a Distinguished Lecturer of the IEEE CAS Society.



Roberto GRANDI got his master degree (MSc) in electrical engineering in 1998 at the Swiss federal institute of technology in Zurich (ETHZ). Since then he has held different functions in the semiconductors industry, with various responsibilities in leading companies (Philips, NXP Semiconductors, DSP Group). His main focus areas, as a member of innovation committees, have been design for testability (DfT), system on chip (SoC) architectures, system in package (SiP) solutions and the whole industrialization cycle (from concept to mass production) of integrated circuit (IC) projects in CMOS technologies down-to 65nm node. Since 2009 he is working as head of engineering and program manager at Aptasic SA in Boudry/NE. In 2010 he got an executive Master of Business Administration (eMBA) in Management and Corporate Finance.



Guido KEEL finished his studies at the ETH Zurich in 1989. He worked several years in the industry designing analog and digital electronics for medical ultrasound and measurement instrumentation, before he started as an analog-IC designer at Fenner ASIC Design Center. Since 1996 he works as Research Fellow at the FHNW. His tasks include project acquisition, planning and management as well as design of mixed signal ASICs and systems. His main interests are in the design and modeling of electronic systems including sensors and actors, low power circuits and signal processing.

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Presenters and their biographies

(afternoon sessions)



Peter KAISER studied at the University of Applied Science in Munich Electronics with the major field Communications Engineering. After his degree as Diplom Ingenieur (FH) he started working with Kontron Electronics as application engineer for electronic CAD systems. In 1990 he started working for Computervision and was attended to key customers in the area simulation of analog and digital circuits. After this, 1993 till 1998, he became an expert in simulation methods for electric and magnetic fields and explored systematical methods to describe and solve unwanted electromagnetic couplings (EMC) at Siemens. To his current field of activity, he came in 1998.

He was leading the development for several mixed signal ASIC's in the area of automotive and industrial. He guided the ASIC's starting with the product idea, continuing with the design and ended with the high volume production. In May 2006 he started his own business and has an engineering company for mixed signal ASIC Design. He is representing the Tanner IC Design Software and Aptasic in Germany.



Leon VAN DE LOGT has a MSc. degree in Applied Semiconductor physics from the University of Technology Eindhoven, The Netherlands. He has a long history in digital and analog test engineering for Mixed signal circuits, SoC and SIP designs. Leon worked in the Philips Research labs on test innovation for a variety of consumer and automotive products.

In 2006 he joined NXP semiconductors where he was responsible for the improvement of analogue testing for volume production and he had a leading role in the test innovation program in NXP. Leon is holding more than 10 patents in the field of digital and analog testing.

In 2009 he initiated the foundation of D4T Systems, a start-up with main focus on the automation and simulation of the complete tester-chip interface. Currently, he is director of the company.



Dr. Pascal SALOME received his degree in electrical engineering from the National Institute of Applied Sciences (INSA) of Lyon, France in 1994.

He received his PhD degree in 1998 for his studies on physical phenomena in NMOS transistors submitted to Electrostatic Discharges.

In 1998, he joined the R&D centre of STMicroelectronics to develop ESD protection structures for advanced sub-micron technologies. In 2000, he led the ESD/LU group for the IO-cell development.

Since 2005, he has been working as program manager and consultant for SERMA technologies.

For detailed information and registration please go to www.ms-asics.ch.

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